	A	A. I.
	Application No.	Applicant(s)
Notice of Allowability	09/660,105	LOSEE ET AL.
Nouce of Anowability	Examiner	Art. Unit
	James M. Hannett	2612
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to <u>The after Final amendment Filed 8/1/2005.</u>		
2. The allowed claim(s) is/are <u>1-6</u> .		
3. The drawings filed on 12 September 2000 are accepted by the Examiner.		
4.		
Attachment(s) 1. Notice of References Cited (PTO-892) 2. Notice of Draftperson's Patent Drawing Review (PTO-948) 3. Information Disclosure Statements (PTO-1449 or PTO/SB/O Paper No./Mail Date 4. Examiner's Comment Regarding Requirement for Deposit of Biological Material	6. ☐ Interview Summary Paper No./Mail Dat 8), 7. ☐ Examiner's Amendn	e

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DETAILED ACTION

Allowable Subject Matter

The following is an examiner's statement of reasons for allowance: The prior art teaches the use of a interline CCD image senor which uses a plurality of phases to drive vertical shift registers in order to read out data. The prior art teaches applying a first set of voltages to the phases of the gate electrodes of the vertical shift registers sufficient to accumulate holes. Furthermore, the prior art teaches applying a second voltage to the first set of gate electrodes while simultaneously applying a more positive voltage to a second set of gate electrodes. However, the prior art does not teach applying an additional positive voltage pulse to the first set of gate electrodes during the period of more positive voltage on the second set of electrodes.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to James M. Hannett whose telephone number is 571-272-7309. The examiner can normally be reached on 8:00 am to 5:00 pm M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Thai Tran can be reached on 571-272-7382. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

James M. Hannett Examiner Art Unit 2612

JMH August 11, 2005

THAI TRANSMIRES